Notice of References Cited Application/Control No. 10/026,560 Examiner Cynthia Britt Applicant(s)/Patent Under Reexamination TAKESHIGE ET AL. Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,480,979	11-2002	Tomari, Nobuhiro	714/724
	В	US-6,546,511	04-2003	Sim et al.	714/724
	С	US-6,550,023	04-2003	Brauch et al.	714/42
	D	US-5,793,774	08-1998	Usui et al.	714/719
	E	US-5,539,699	07-1996	Sato et al.	365/201
	F	US-			
	G	US-			
	Н	US-			
		US-			
	J	US-			
•	к	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*	-	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N				,	
	0					
	Р					
	α					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	υ	"Processor-Programmable Memory BIST for Bus-connected Embedded Memories" Tsai et al. Design Automation Conference Proceedings, 2001. Publication Date: 30 Jan2 Feb. 2001 pages: 325 - 330 Inspec Accession Number: 6924510
	>	
	W	
	х	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.